

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kazuyuki Yamashita **Examiner:** Unassigned
Serial No: To be assigned **Art Unit:** Unassigned
Filed: Herewith **Docket:** 16810
For: DATA PROCESSOR, SEMICONDUCTOR **Dated:** July 11, 2003
MEMORY DEVICE AND CLOCK FREQUENCY
DETECTING METHOD

Commissioner for Patents
United States Patent Office
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following reference, which is also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Patent Publication No. 2002-055130, dated February 20, 2002, together with an English language Abstract.

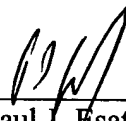
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Dated: July 11, 2003

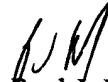


Paul J. Esatto, Jr.

Applicant is submitting a copy of the above-cited reference. The relevance of the above-identified reference has been described in the specification.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,



Paul J. Esatto, Jr.

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PJE/sf

Enclosure

PTO 1449

One (1) reference

**LIST OF PRIOR ART
CITED BY APPLICANT**

(Use several sheets if necessary)

Atty. Docket No.
16810

Serial No.
To be assigned

Applicant
Kazuyuki Yamashita

Filing Date
Herewith

Group
Unassigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL*		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)
	AA						
	AB						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		2002-055130	2/20/2002	JAPAN			YES	

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.